FIG. 1A

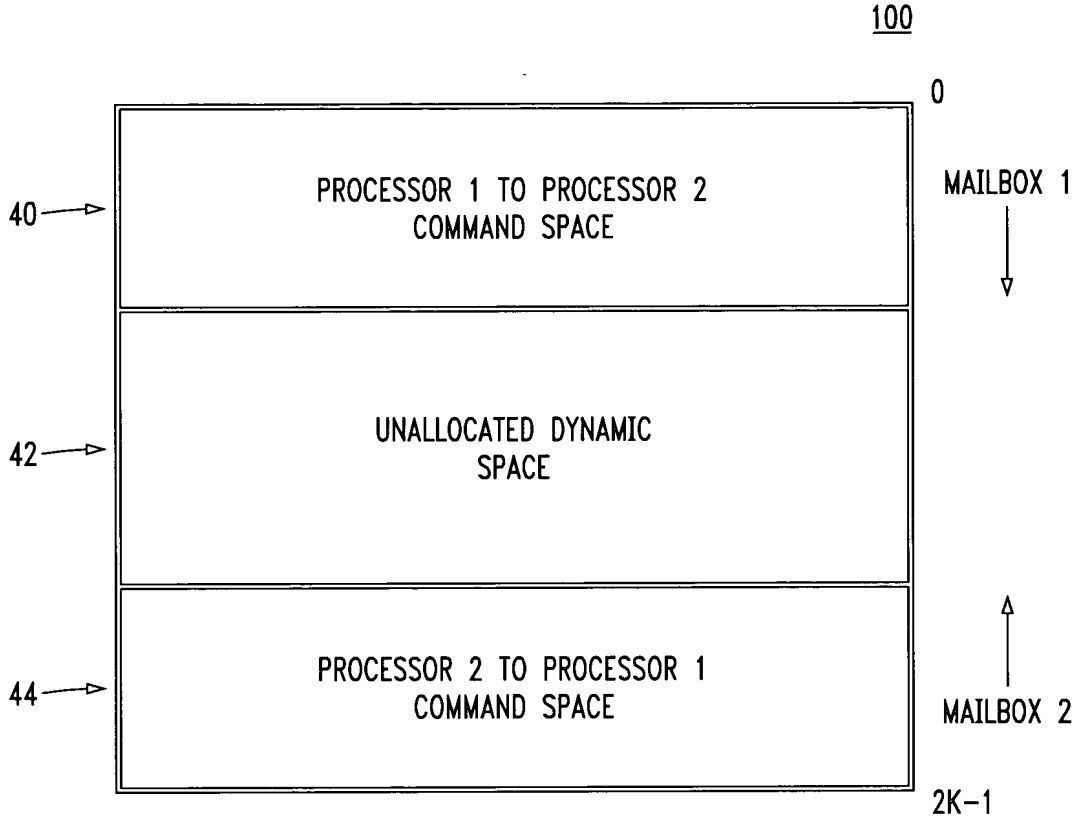
100
0
PROCESSOR 1 TO PROCESSOR 2
COMMAND SPACE
MAILBOX 1
40
UNALLOCATED DYNAMIC
SPACE
42
PROCESSOR 2 TO PROCESSOR 1
COMMAND SPACE
44
MAILBOX 2
2K-1

FIG. 1B

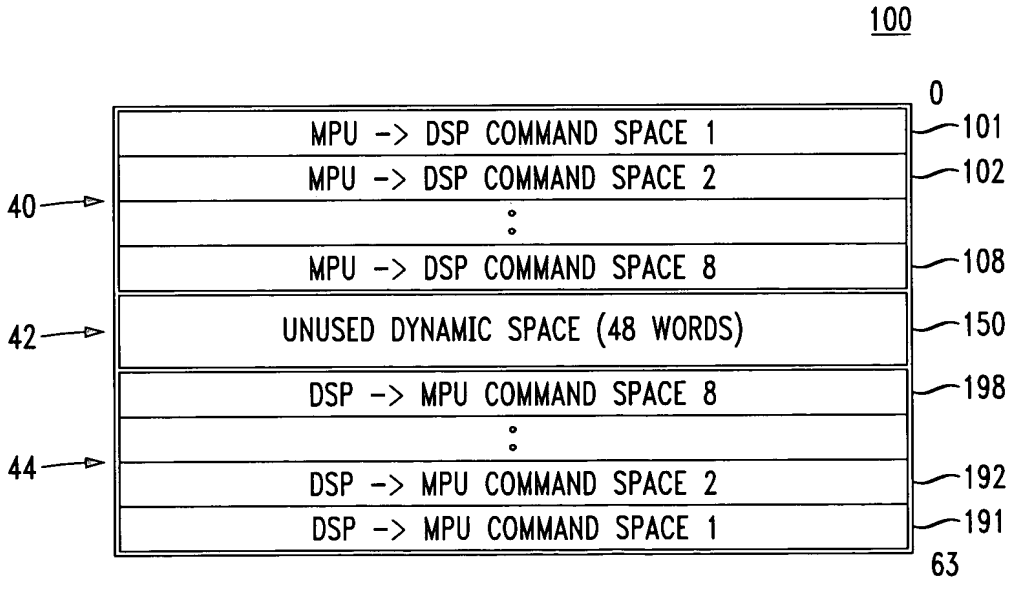
100
0
MPU -> DSP COMMAND SPACE 1
101
MPU -> DSP COMMAND SPACE 2
102
40
MPU -> DSP COMMAND SPACE 8
108
UNUSED DYNAMIC SPACE (48 WORDS)
150
42
DSP -> MPU COMMAND SPACE 8
198
44
DSP -> MPU COMMAND SPACE 2
192
DSP -> MPU COMMAND SPACE 1
191
63

FIG. 2

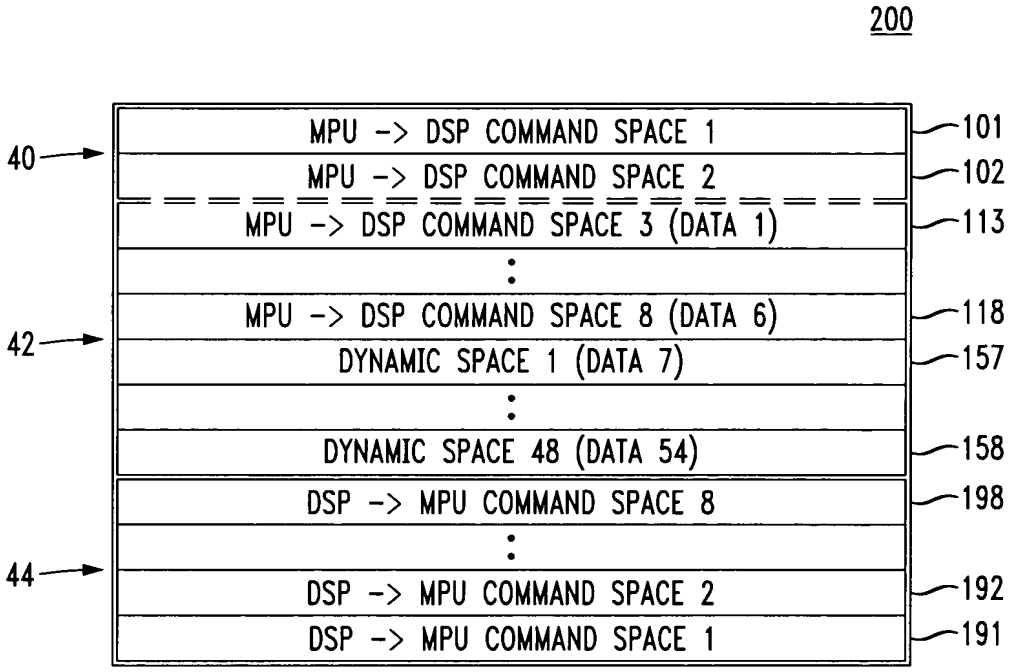
200
MPU -> DSP COMMAND SPACE 1
MPU -> DSP COMMAND SPACE 2
MPU -> DSP COMMAND SPACE 3 (DATA 1)
MPU -> DSP COMMAND SPACE 8 (DATA 6)
DYNAMIC SPACE 1 (DATA 7)
DYNAMIC SPACE 48 (DATA 54)
DSP -> MPU COMMAND SPACE 8
DSP -> MPU COMMAND SPACE 2
DSP -> MPU COMMAND SPACE 1
40
42
44
101
102
113
118
157
158
198
192
191

FIG. 3

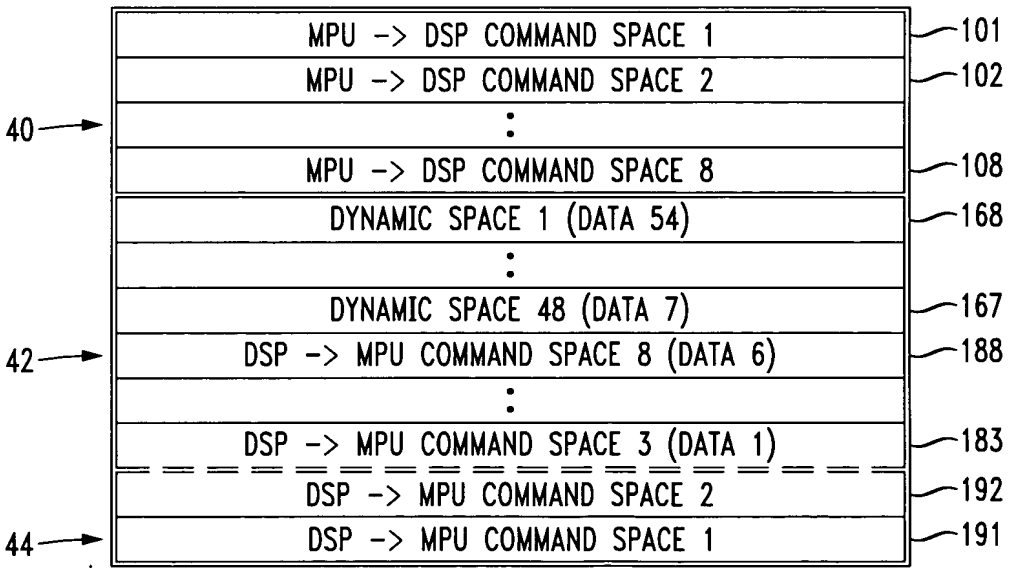
300
MPU -> DSP COMMAND SPACE 1
MPU -> DSP COMMAND SPACE 2
MPU -> DSP COMMAND SPACE 8
DYNAMIC SPACE 1 (DATA 54)
DYNAMIC SPACE 48 (DATA 7)
DSP -> MPU COMMAND SPACE 8 (DATA 6)
DSP -> MPU COMMAND SPACE 3 (DATA 1)
DSP -> MPU COMMAND SPACE 2
DSP -> MPU COMMAND SPACE 1
40
42
44
101
102
108
168
167
188
183
192
191

FIG. 4
PRIOR ART

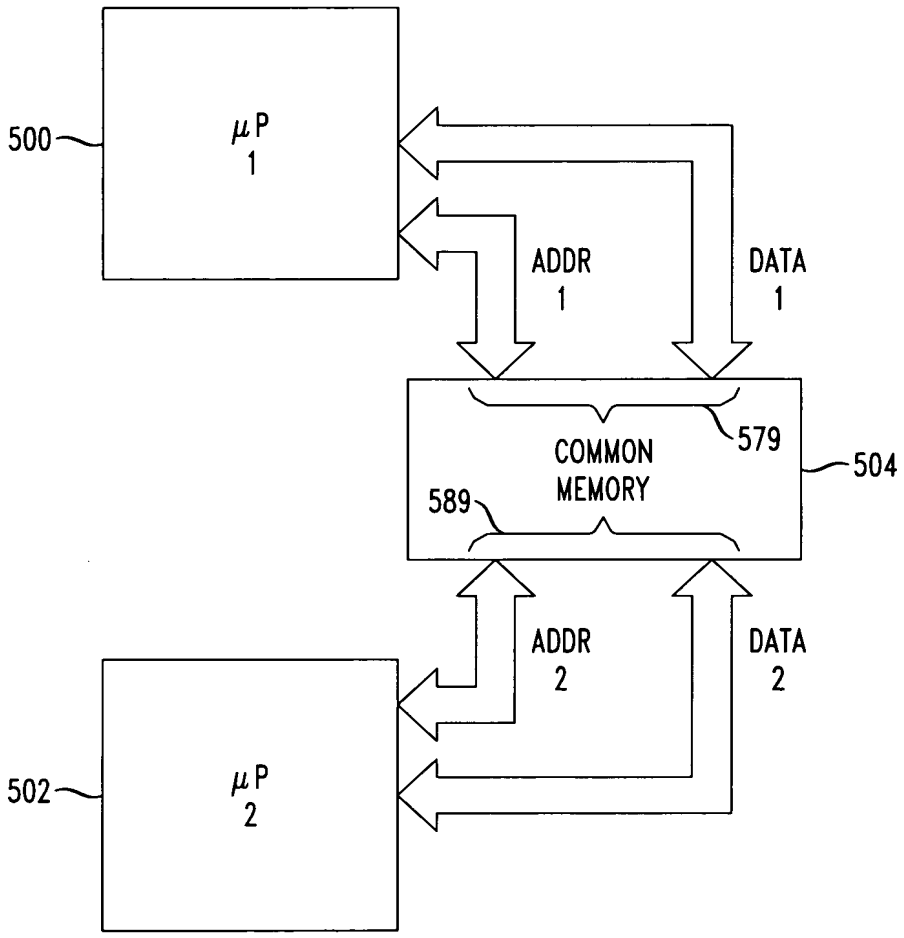

FIG. 5
PRIOR ART

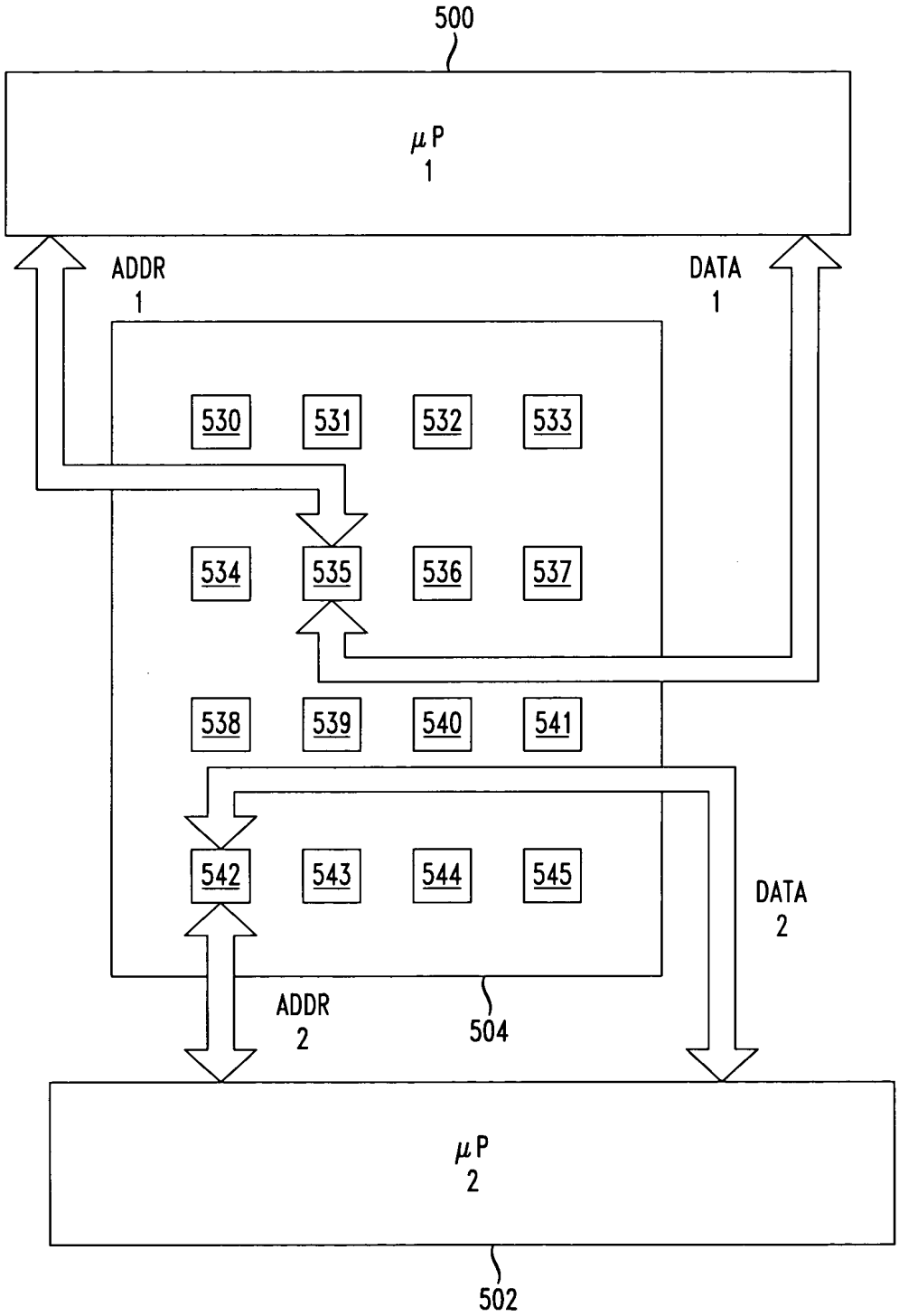
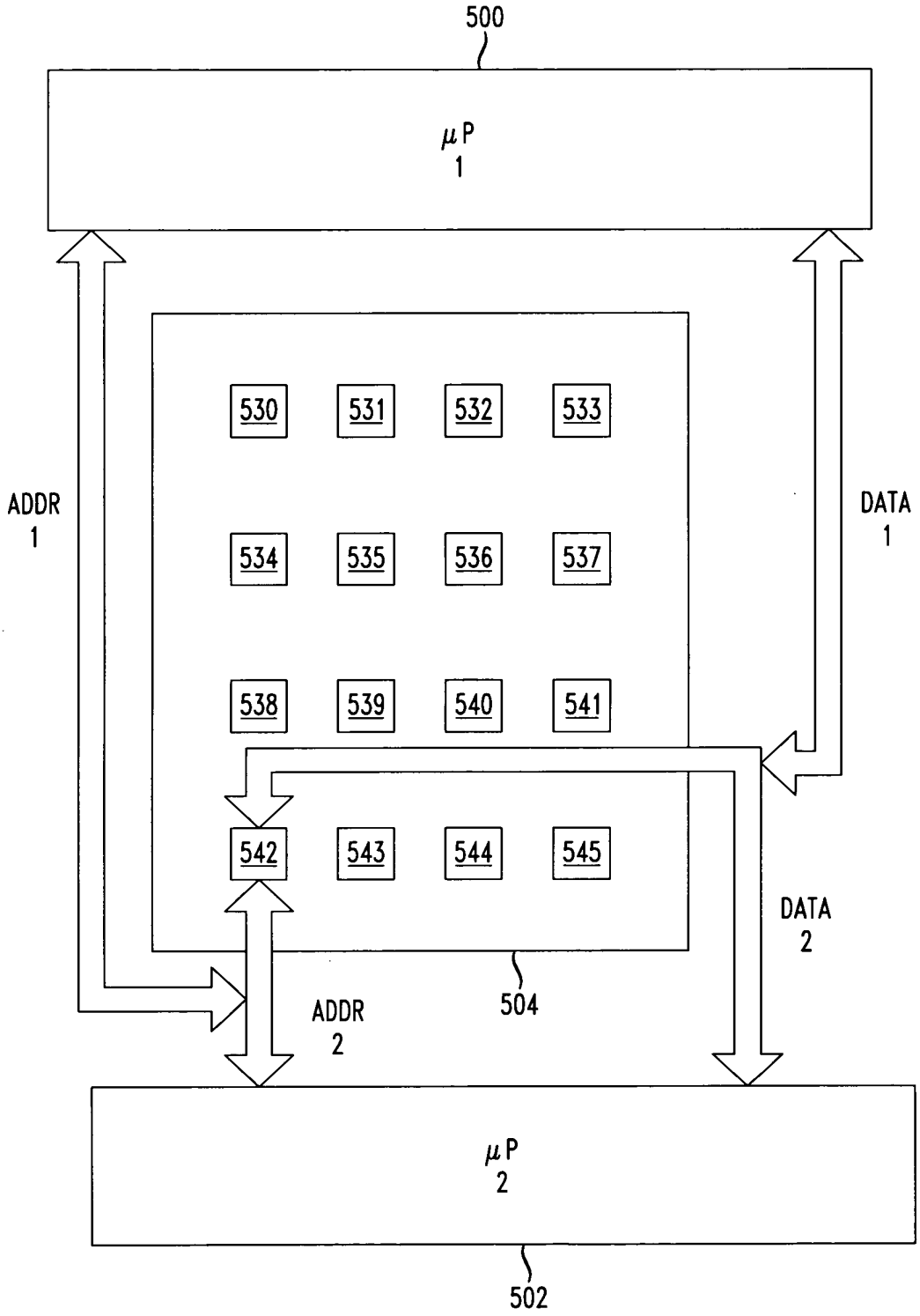
FIG. 6
PRIOR ART